

J309, J310 Datasheet

Preferred Device

J FET VHF/UHF Amplifiers N-Channel — Depletion

Features

- Pb-Free Packages are Available*

MAXIMUM RATINGS

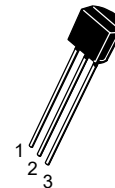
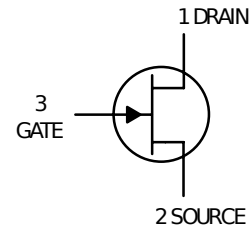
Rating	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	25	Vdc
Gate-Source Voltage	V_{GS}	25	Vdc
Forward Gate Current	I_{GF}	10	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above = 25°C	P_D	350 2.8	mW mW/ $^\circ\text{C}$
Junction Temperature Range	T_J	-65 to +125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-65 to +150	$^\circ\text{C}$

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



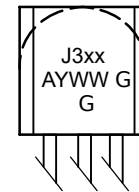
ON Semiconductor®

<http://onsemi.com>



TO-92
CASE 29-11
STYLE 5

MARKING DIAGRAM



J3xx = Device Code
xx = 09 or 10

A = Assembly Location

Y = Year

WW = Work Week

G = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

<https://www.mikeselectronicparts.com>

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Gate-Source Breakdown Voltage (I _G = -1.0 nAdc, V _{DS} = 0)	V _{(BR)GSS}	-25	-	-	Vdc
Gate Reverse Current (V _{GS} = -15 Vdc, V _{DS} = 0, T _A = 25°C) (V _{GS} = -15 Vdc, V _{DS} = 0, T _A = +125°C)	I _{GSS}	-	-	-1.0 -1.0	nAdc nAdc
Gate Source Cutoff Voltage (V _{DS} = 10 Vdc, I _D = 1.0 nAdc)	V _{GS(off)}	-1.0 -2.0	-	-4.0 -6.5	Vdc
ON CHARACTERISTICS					
Zero-Gate-Voltage Drain Current ⁽¹⁾ (V _{DS} = 10 Vdc, V _{GS} = 0)	I _{DSS}	12 24	-	30 60	mAdc
Gate-Source Forward Voltage (V _{DS} = 0, I _G = 1.0 mAdc)	V _{GS(f)}	-	-	1.0	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Common-Source Input Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 MHz)	Re(y _{is})	-	0.7 0.5	-	mmhos
Common-Source Output Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 MHz)	Re(y _{os})	-	0.25	-	mmhos
Common-Gate Power Gain (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 MHz)	G _{pg}	-	16	-	dB
Common-Source Forward Transconductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 MHz)	Re(y _{fs})	-	12	-	mmhos
Common-Gate Input Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 MHz)	Re(y _{ig})	-	12	-	mmhos
Common-Source Forward Transconductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 1.0 kHz)	g _{fs}	10000 8000	-	20000 18000	mmhos
Common-Source Output Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 1.0 kHz)	g _{os}	-	-	250	mmhos
Common-Gate Forward Transconductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 1.0 kHz)	g _{fg}	-	13000 12000	-	mmhos
Common-Gate Output Conductance (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 1.0 kHz)	g _{og}	-	100 150	-	mmhos
Gate-Drain Capacitance (V _{DS} = 0, V _{GS} = -10 Vdc, f = 1.0 MHz)	C _{gd}	-	1.8	2.5	pF
Gate-Source Capacitance (V _{DS} = 0, V _{GS} = -10 Vdc, f = 1.0 MHz)	C _{gs}	-	4.3	5.0	pF
FUNCTIONAL CHARACTERISTICS					
Equivalent Short-Circuit Input Noise Voltage (V _{DS} = 10 Vdc, I _D = 10 mAdc, f = 100 Hz)	\bar{e}_n	-	10	-	nV $\sqrt{\text{Hz}}$

1. Pulse Test: Pulse Width \leq 300 ns, Duty Cycle \leq 3.0%.

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ORDERING INFORMATION

Device	Package	Shipping†
J309	TO-92	1000 Units / Bulk
J309G	TO-92 (Pb-Free)	
J310	TO-92	1000 Units / Bulk
J310G	TO-92 (Pb-Free)	
J310RLRP	TO-92	2000 Units / Tape & Ammo Box
J310RLRPG	TO-92 (Pb-Free)	
J310ZL1	TO-92	2000 Units / Tape & Ammo Box
J310ZL1G	TO-92 (Pb-Free)	

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

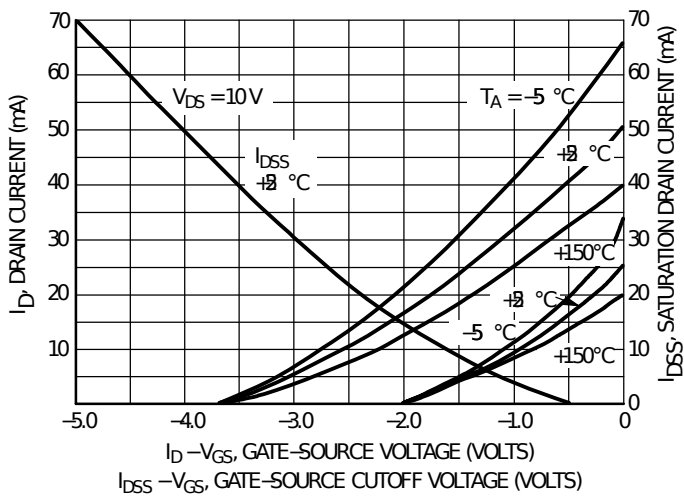


Figure 1. Drain Current and Transfer Characteristics versus Gate-Source Voltage

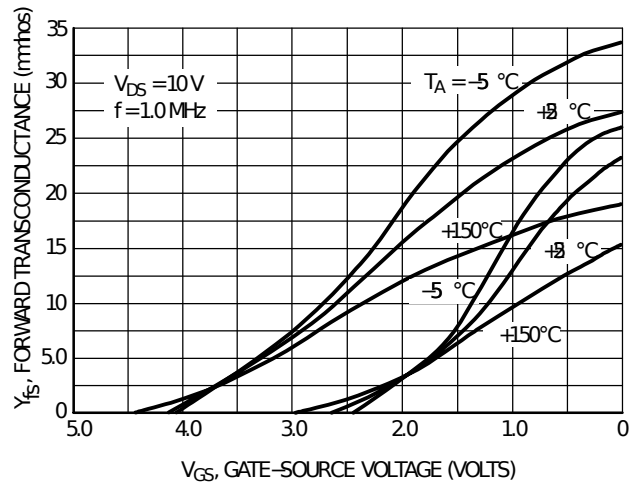


Figure 2. Forward Transconductance versus Gate-Source Voltage

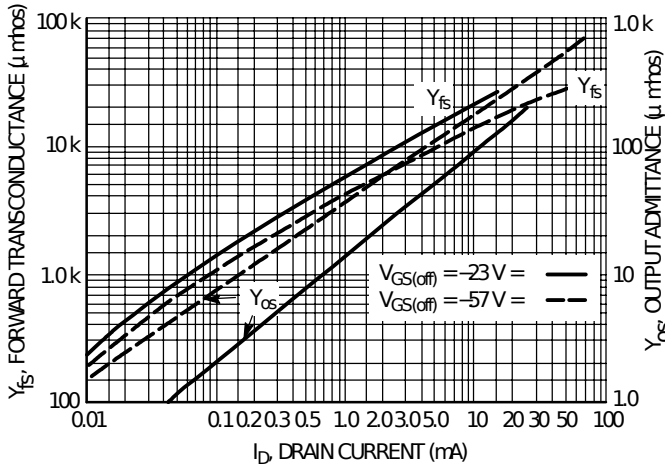


Figure 3. Common-Source Output Admittance and Forward Transconductance versus Drain Current

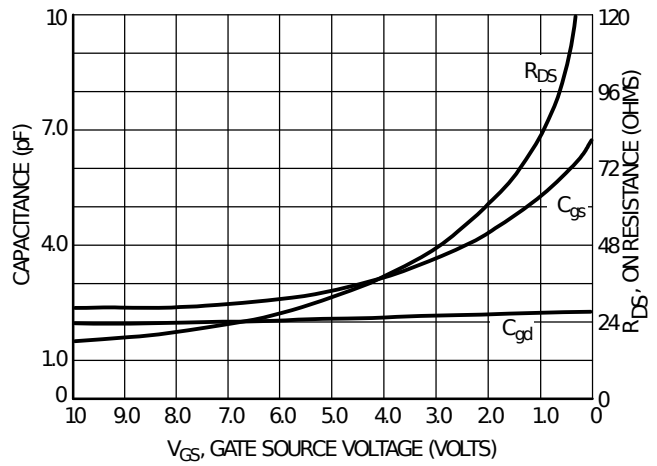


Figure 4. On Resistance and Junction Capacitance versus Gate-Source Voltage

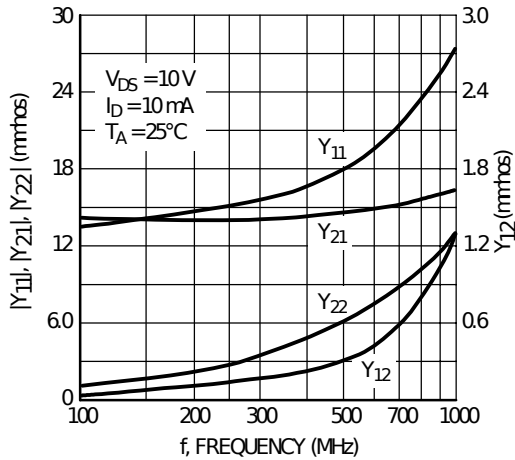


Figure 5. Common-Gate Y Parameter Magnitude versus Frequency

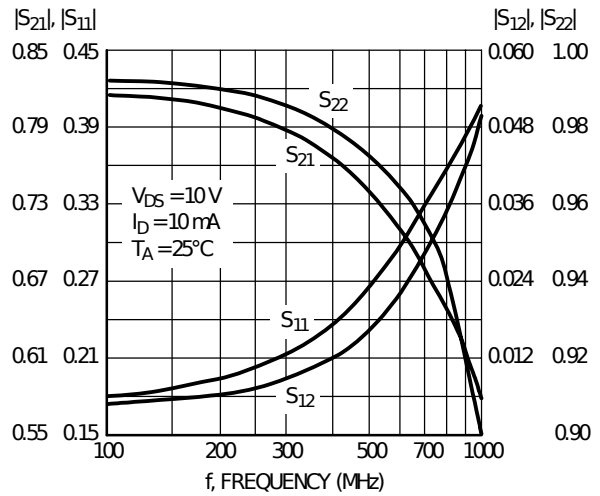


Figure 6. Common-Gate S Parameter Magnitude versus Frequency

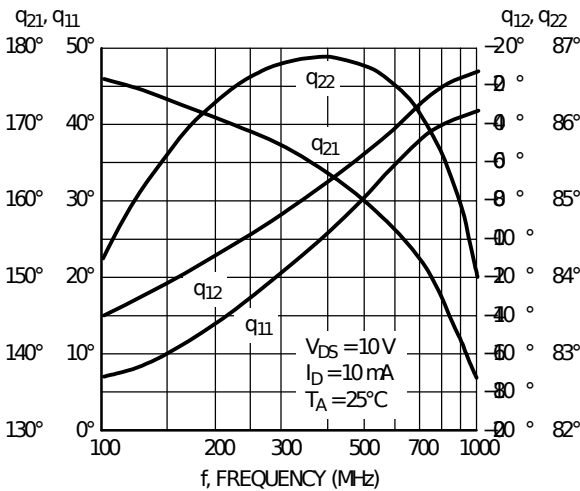


Figure 7. Common-Gate Y Parameter Phase-Angle versus Frequency

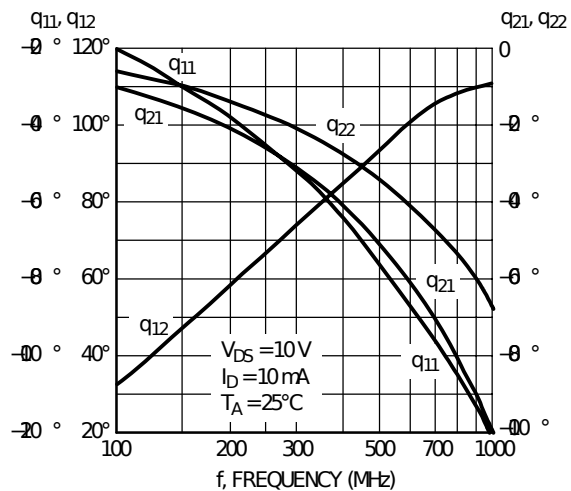
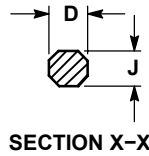
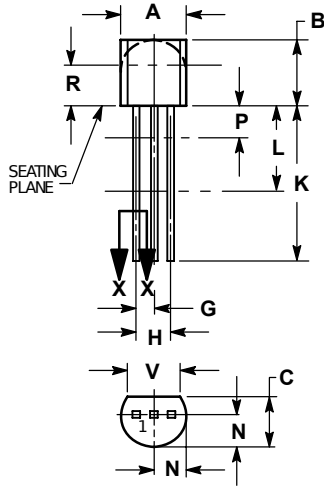


Figure 8. S Parameter Phase-Angle versus Frequency

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PACKAGE DIMENSIONS

TO-92 (TO-226)
CASE 29-11
ISSUE AL




NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	—	12.70	—
L	0.250	—	6.35	—
N	0.080	0.105	2.04	2.66
P	—	0.100	—	2.54
R	0.115	—	2.93	—
V	0.135	—	3.43	—

STYLE 5:

1. DRAIN
2. SOURCE
3. GATE

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